GP/2123

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s):   J. G. Walacavage et al.

Docket No.
200-0664

Serial No.	Filing Date	Examiner	Group Art Unit
09/965,905	September 28, 2001		2123

Invention: **METHOD OF PART FLOW MODEL FOR PROGRAMMABLE LOGIC CONTROLLER LOGICAL VERIFICATION SYSTEM**

OIPE
JAN 02 2002

RECEIVED

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

JAN 0 7 2002

Transmitted herewith is an amendment in the above-identified application.
The fee has been calculated and is transmitted as shown below.

Technology Center 2100

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	21 -	21 =	0	x $18.00	$0.00
INDEP. CLAIMS	3 -	3 =	0	x $84.00	$0.00
Multiple Dependent Claims (check if applicable) ☐					$0.00
		TOTAL ADDITIONAL FEE FOR THIS AMENDMENT			$0.00

- ☒ No additional fee is required for amendment.
- ☐ Please charge Deposit Account No.   in the amount of
  A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of   to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No.   **06-1510**
  A duplicate copy of this sheet is enclosed.
  - ☒ Any additional filing fees required under 37 C.F.R. 1.16.
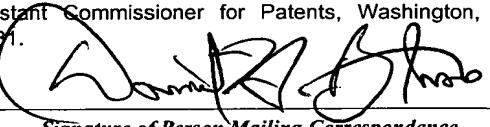 - ☒ Any patent application processing fees under 37 CFR 1.17.

_____
        Signature

Dated: **November 1, 2001**

Daniel H. Bliss (Reg. No. 32,398) [0693.00242]
Bliss McGlynn & Nolan, P.C.
2075 West Big Beaver Road, Suite 600
Troy, Michigan 48084
(248) 649-6090

I certify that this document and fee is being deposited on November 1, 2001   with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

_____
Signature of Person Mailing Correspondence

Daniel H. Bliss

Typed or Printed Name of Person Mailing Correspondence

CC:

P11LARGE/REV06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
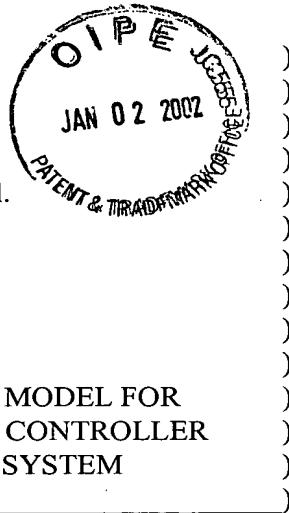
Art Unit: 2123)
)
Examiner:)
)
Applicant(s): J. G. Walacavage et al.)
)
Serial No.: 09/965,905)
)
Filing Date: September 28, 2001)
)
For: METHOD OF PART FLOW MODEL FOR)
    PROGRAMMABLE LOGIC CONTROLLER)
    LOGICAL VERIFICATION SYSTEM)
)

**PRELIMINARY
AMENDMENT**

RECEIVED

JAN 0 7 2002

Technology Center 2100

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Prior to examination, please amend the above-identified patent application as follows:

IN THE SPECIFICATION:

Please replace the pending paragraph beginning on page 12, line 6 through page 13, line 2 with the following:

From block 108, the method advances to bubble 116 and moves the part from the fixture to block 118 and tests the part. The user 12 tests the logic by forcing a state in the control logic to test all exception logic. For example, the method tests for status as to whether the part is present or not present. After block 118, the method then ends. It should be appreciated that part locations have exit conditions that are interlocked. It should also be appreciated that a record exists with each part generated and that the individual resources can contribute information to the